

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/045,307	DeWitt et al.	
		Examiner	Art Unit	Page 1 of 1
		Joshua A Lohn	2114	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0100023 A1	07-2002	Ueki et al.	717/127
	B	US-5,878,208 A	03-1999	Levine et al.	714/38
	C	US-6,507,921 B1	01-2003	Buser et al.	714/45
	D	US-6,732,307 B1	05-2004	Edwards, David Alan	714/724
	E	US-6,728,906 B1	04-2004	Case et al.	714/45
	F	US-5,970,246 A	10-1999	Moughani et al.	717/128
	G	US-2003/0135789 A1	07-2003	DeWitt et al.	714/38
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM Technical Disclosure Bulletin NN961231; Processor Single Step Trace Facility Enhancements; volume 39; issue 12; pages 31-32, published December 1, 1996
	V	Tanenbaum; Structured Computer Organization; 1990; Prentice-Hall, Inc.; Third Edition, pages 11-13
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.